ECE3201

HW # 12 due November 17, 2014

Electronic Circuit Design and Analysis

Exam #3 will be on Monday, December 1, 2014

On FET's

Chapter 8 problems: 36, 45

Chapter 9 Problems 7, 9, 11, 31

Book: Electronic Devices and Circuit Theory by Robert L Boylestad and Louis Nashelsky; Eleventh Edition