

ECE3201

HW # 13 due December 3, 2014

Electronic Circuit Design and Analysis

Chapter 9 problems:

36

Chapter 12 Problems

2, 5, 6, 12

Exam 3 on December 1st to include DC and AC analysis of
FET's and Plotting.

It will be an open book exam – no notes.

Final Exam will be all-inclusive of work done in class

Book: Electronic Devices and Circuit Theory
by Robert L Boylestad and Louis Nashelsky; Eleventh Edition