

ECE 249 Lab 6

Spring 2005

This lab is a two week lab due on March 30th. You will design a register, another component that you can use for your final project. You can use the design from lecture 8. The register will have two new subcomponents that you will have to design: a transmission gate, and a latch.

Hand in schematics, layout, simulation results, and LVS reports for the transmission gate, the latch, and the register. Also, determine the hold time, setup time and propagation delays for the register.